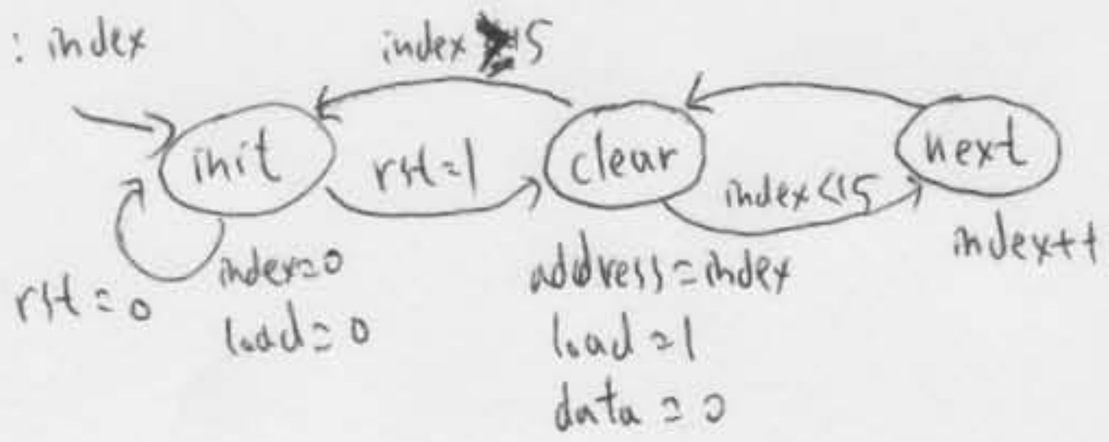
5.4

Input : rst

Outputs: address, load, data

register : index



5.6

inputs: cnt. clr

outputs: tc

register : sum